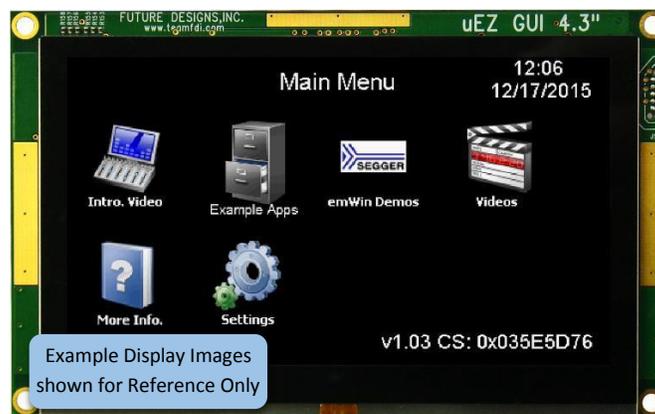


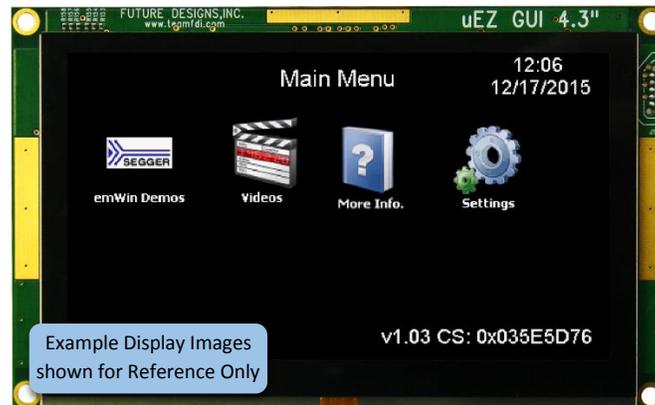
μEZ[®] GUI User's Manual

Covers the following products:

- uEZGUI-4088-43WQN (Dev Kit)
- uEZGUI-4088-WQN-BA (Board Assembly)
- uEZGUI-4088-43WQH (Dev Kit)
- uEZGUI-4088-WQH-BA (Board Assembly)
- uEZGUI-4088-43WQE (Dev Kit)
- uEZGUI-4088-WQE-BA (Board Assembly)



43WQN and 43WQH Main Menu



43WQE Main Menu

NOTICE: At FDI we are constantly improving our documentation. Please get the latest version of this document in the documentation tab of the product page at www.TeamFDI.com

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1. Introduction

The UEZGUI-4088-43WQN-BA/UEZGUI-4088-43WQH-BA/UEZGUI-4088-43WQE provide a quick and easy solution for implementing a Graphical User Interface (GUI) based design by providing the basic functions necessary for most customer products.

Below is a table showing the differences between the 3 part numbers – ALL other specifications are identical.

Part Number	Touch Type	Notes
UEZGUI-4088-43WQN	PCAP	PCAP interface uses I2C interface
UEZGUI-4088-43WQH	4-wire Resistive	Resistive uses GPIO & ADC interface
UEZGUI-4088-43WQE	No Touch	No interfaces utilized

Note: In this document, whenever UEZGUI-4088-WQN is mentioned, it is assumed to include the UEZGUI-4088-WQH & UEZGUI-4088-WQE unless otherwise specified.

2. Block Diagram

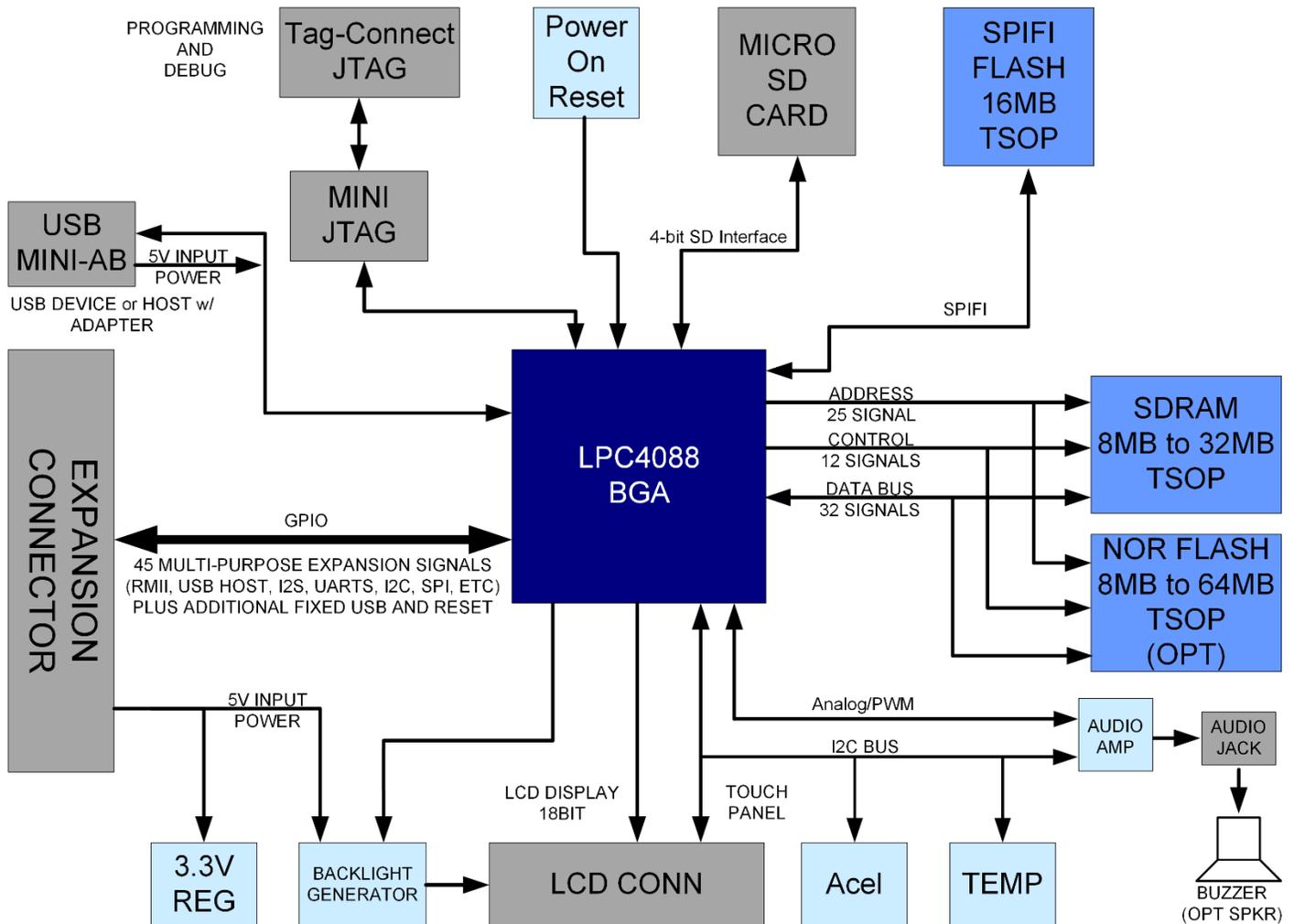


Figure 1 – UEZGUI-4088-43WQN-BA Block Diagram

3. Kit Contents

- UEZGUI-4088-43WQN-BA module with 4.3" Capacitive Touch LCD Screen
or
- UEZGUI-4088-43WQH-BA module with 4.3" 4-Wire Resistive Touch LCD Screen
or
- UEZGUI-4088-43WQE-BA module with 4.3" No-touch LCD Screen
- Segger Mini-JTAG Debugger and cable
- FDI JTAG Adapter Board
- Micro SD card
- USB Power/Data Cable (A to mini B)
- AC to USB Power Supply
- Quick Start Guide

4. Useful links

Complete User's Manuals, Schematics, and documentation are available on the Micro-SD card provided with the μEZGUI Kit and are also available from the following websites (please refer to the websites for the latest updates):

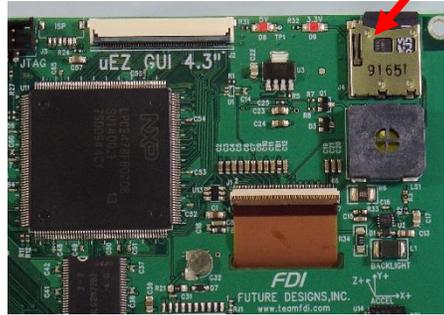
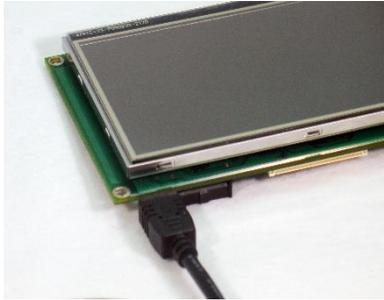
- Future Designs Support Page - <http://www.teamfdi.com/support/>
- μEZ Source Code, user's manuals, and quickstart guides - <https://sourceforge.net/projects/uez/>
- Segger Mini-JTAG Debugger - <http://www.segger.com/cms/jlink-software.html>
- Rowley Crossworks IDE download for 30-day evaluation - <http://www.rowley.co.uk/arm/index.htm>

5. Functional Description

- LPC4088 120 MHz Cortex-M4 based Microprocessor
- SDRAM 8MB, optional up to 64MB
- SPIFI Serial Flash 16MB
- Internal on-chip 4KB EEPROM
- RTC – Real Time Clock with Super Capacitor backup
- Temperature Sensor
- 3-axis Accelerometer
- Speaker
- Micro SD Card Socket for up to 64GB SDHC storage, microSD card included with kit only
- Mini JTAG
- PMOD Type 2A connector
- Power-on Reset Generator - power-on reset supervisor and voltage monitor (SW1)
- Expansion Connector for customer specific applications

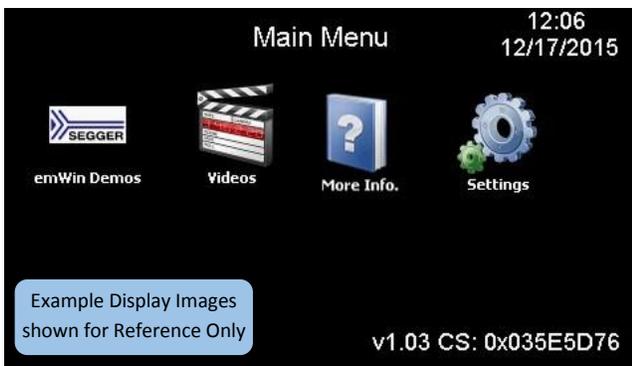
6. Startup procedure

The μEZ GUI kit comes with a pre-installed 4GB micro SD card that contains files required for the demo applications and slide show to run. It also contains users' manuals, schematics, and documentation for the product.

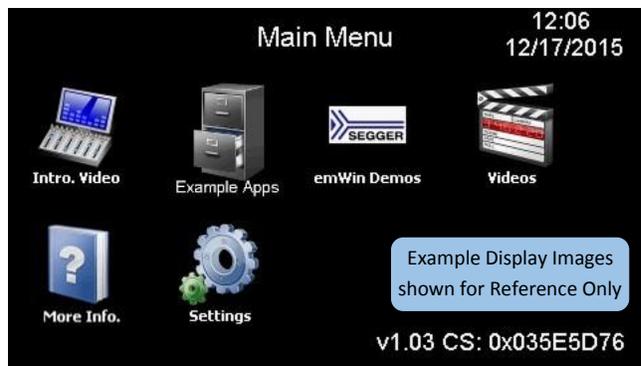


Power is supplied via the USB cable provided in the kit. To power on the unit from the USB power supply or other USB source, connect the USB cable to P1, the mini AB USB connector.

Depending on the version you are using, one of the following screens should appear once power has been applied to the kit:



43WQE Screen



43WQH and 43WQN Screen

The unit is now ready for software demonstrations and user operation.

By connecting the USB A to mini B cable to P1, the μEZ GUI will also appear as a USB Flash Drive to the PC, allowing the user to read/write files directly to the Micro SD card.

7. Demonstration Software Main Menu

Note: The Demonstration Software is subject to change at any time.

The following software demonstrations are available from the Main Menu:

a) Example Apps

Selecting the Example Apps icon will load the Example Applications Screen with the following options:

- **Main Menu** – this will return the μEZ GUI to the Main Menu of the demonstration software. The μEZ GUI will load the application and reboot.

- **Bowling** – This demonstration is an example of a bowling alley user interface.
- **Project Maker** – This is a demonstration example application developed using the Project Maker utility from Future Designs. Currently, this demonstration is a Temperature and Time/Date application.
 - **Temperature**

Select the temperature icon to display external and internal temperatures. The μEZ GUI will display an external temperature at any time. However, it is only valid when an external temperature sensor has been connected to the μEZ GUI hardware. [Diligent Inc.](#) sells a temperature sensor, [part number 410-287P](#), that could be used for this purpose. The internal temperature is read from the [LM75 temperature sensor](#). There are no user changeable parameters. To return to the main menu, select 'Back'.
 - **Time and Date**

Select this feature to display the current time and date from the internal real time clock (RTC).

 - To change the time or date:
 1. Select the field you want to change.
 2. Enter the numeric value.
 3. Select 'Enter'.
 4. Select 'Cancel' if you do not want to change the field. Or, wait until the entry screen times out and returns to the Time Date Settings screen.
 - If invalid values are entered, the software will default to the maximum valid value for the field.
 - Once set, an on-board super capacitor will back-up the time and date for several days (typically) while the unit is powered off.
 - Select 'Back' to return to the Settings Menu.
- **Temp. Graph** – This is a demonstration example of a graphing application recording temperature and humidity.

b) Slideshow

Selecting the slideshow icon will use the microSD card to read slideshow configuration. This allows the user to select between several slideshow options such as “μEZ GUI Family Overview”, “Demonstration Pictures”, “μEZ Software Overview”, “Modular Development Kit”, etc. depending on the configuration of the SD Card.

Select the slideshow you would like to view by touching the menu entry for it on the touch screen. The slide show will auto-play by default.

By touching the screen during the slideshow, the menu overlay will appear:

- Select the “||” button to pause the slideshow.
- Select the play button to begin the automated slide show.
- The user can move to the next slide by touching the “>” or “<” menu buttons (during auto-play or manual sequencing)
- Select the stop (square block) to return to the slideshow menu.
- Select 'Back' from the slideshow menu to return to the previous menu.

c) Settings

Brightness

Selecting the brightness icon shows a menu with color bars and a brightness adjustment slider.

- Move the slider up or down to adjust the brightness.
- Select 'Back' to return to the Settings Menu.

Time/Date

Use this application to set the time and date from the internal real time clock (RTC). This is the time and date displayed in the Settings menu upper right corner.

- To change the time or date simply select the field you want to change:
 1. Enter the numeric value
 2. Select 'Enter'
 3. Select 'Cancel' if you do not want to change the field, or simply wait until the entry screen times out and returns to the Time Date Settings screen
- If invalid values are entered, the software will default to the maximum valid value for the field.
- Once set an on-board super capacitor will back-up the time and date for several days (typically) while the unit is powered off.
- Select 'Back' to return to the Settings Menu.

Vol. Contr.

This feature displays the 'Master', 'Speaker', and 'Headphones' volume sliders, along with 'Mute' checkboxes. 'Master' controls the volume of the unit as a whole, 'Speaker' controls the volume coming from the onboard speaker, and 'Headphones' controls the audio being output if your board has a headphone jack installed.

8. Setting up a Slideshow

We have created a document and included tools for creating your own slideshows for μEZ GUI units. Production module units require 24-bit uncompressed Targa (.TGA) format for all images. Development kit units require bitmap (.BMP) format for all slideshow images.

<http://www.teamfdi.com/wp-content/uploads/Slideshow-Creation-Guide.pdf>

This guide also covers making speaker notes that can play alongside your slides.

9. Setting up a Video

Suggested procedures on creating videos for playback using the μEZ video player provided in μEZ v2.05 (and later) on supported μEZ GUI hardware, is available in the FDI Video Creation Guide (link below). This guide also suggests basic procedures for downloading videos from YouTube.

This guide assumes that a source video is available in AVI uncompressed format with a resolution of 480×272 or greater.

Video Creation Guide:

<http://www.teamfdi.com/support/uez/Video/uEZ%20Auto%20Video%20Conversion%20Guide.pdf>

10. μEZ Doxygen online HTML documentation

μEZ has built-in comment documentation that follows the Doxygen comment standard. This standard writes code comments and annotations in a certain manner so that it is compiled along with source code into HTML documentation.

FDI provides pre-compiled HTML documentation at this link: <http://www.teamfdi.com/uez/docs/>

In the μEZ source code, “uEZ/uEZDoxyfile” is the main project file for the Doxygen generator. When Doxygen is recompiled, the new Doxygen files will be found here: uEZ\Docs\ Doxygen_Documentation.html

FDI updates the documentation periodically. We recommend reviewing your device for updates to the documentation as you learn a new μEZ® release or drivers are added to the system.

For more information, see the Doxygen website: <http://www.doxygen.org/>.

11. μEZ Project Maker

FDI has provided a project maker to help create new projects for μEZ GUI hardware. It is available for download at <http://sourceforge.net/projects/uez/>.

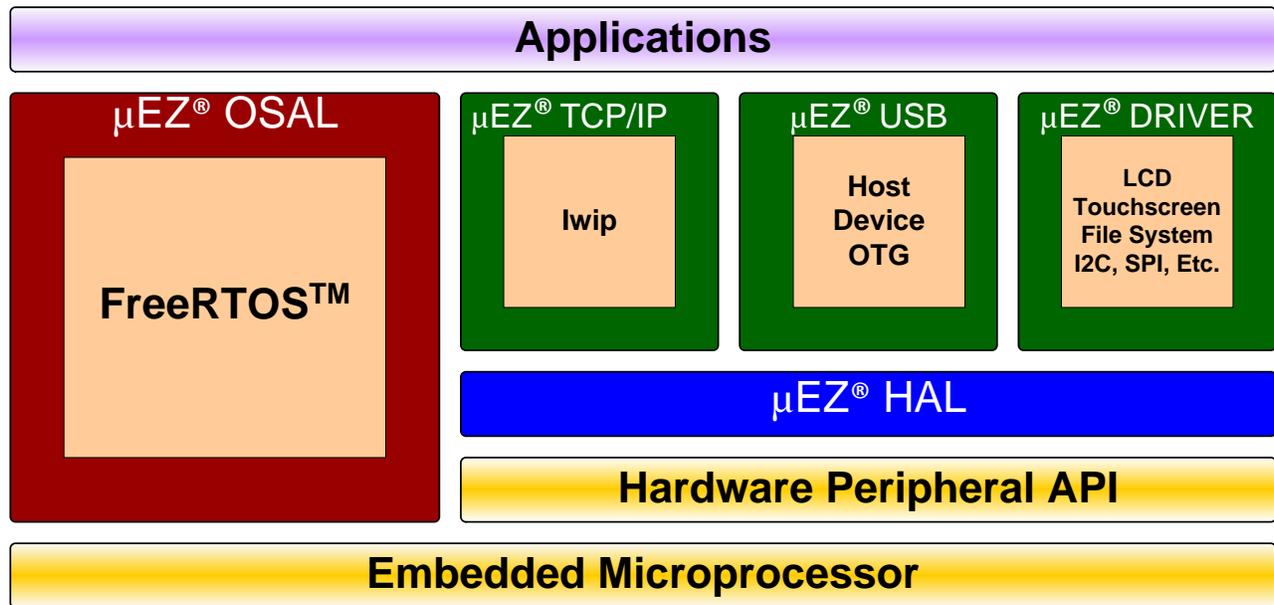
To create a new project, download the application, run the executable file, and follow the onscreen instructions. It will create a demo project using an emWin example GUI that will provide basic peripheral functionality. The project maker greatly speeds up the development process for new applications. Example projects are ready to be compiled and programmed onto μEZ GUI hardware using the included J-Link debugger, with no extra project configuration necessary.

12. Software

μEZ® takes its name from the Muses of Greek mythology. A Muse was a goddess who inspired the creation process for the arts and sciences. Like its ancient Greek namesake, the μEZ® platform inspires rapid development by supplying customers with an extensive library of open source software, drivers, and processor support - all under a common framework. μEZ® development works on the premise of “design once, reuse many times”. This provides an open source standard for embedded developers to build upon and support. μEZ® allows companies to focus on innovation and on their own value-added applications while minimizing development time and maximizing software reuse.

The diagram below shows a typical embedded application stack. μEZ® has three primary categories of components that help simplify embedded application development:

1. **Operating System Abstraction Layer (μEZ® OSAL)**
2. **Sub-system drivers (μEZ® TCP/IP, μEZ® USB, μEZ® Driver)**
3. **Hardware Abstraction Layer (μEZ® HAL)**



The selection of an RTOS can be one of the most daunting aspects of an embedded system development. With **μEZ[®]** the primary features of common multi-tasking operating systems are abstracted, thus easing the transition to an open source or low-cost RTOS. The **μEZ[®]** OSAL provides applications access to the following features in an OS-independent fashion:

- Pre-emptive multitasking
- Stack overflow detection
- Unlimited number of tasks
- Queues
- Semaphores (binary, counting, mutex)

The **μEZ[®]** sub-system drivers utilize the OSAL functions to provide protected access to the processor peripherals. The sub-system driver API functions are typically protocol layer interfaces (TCP/IP, USB, etc) designed as high-level access routines such as open, close, read, write, etc. where possible.

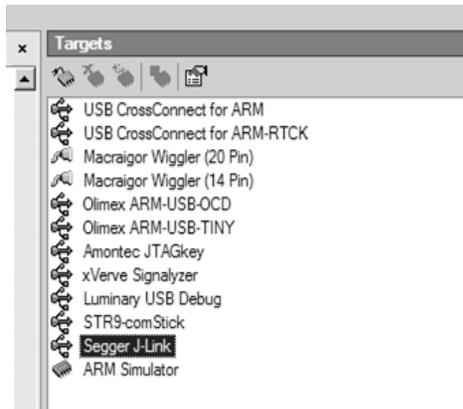
μEZ[®] is ideally suited for Embedded Systems with standard features such as:

- Processor and Platform BSPs (Board Support Packages)
- Real Time Operating System (RTOS)
- Memory Management
- NAND/NOR Flash
- SDRAM and DDR Memory
- TCP/IP stack
- USB Device/Host Libraries
- Mass Storage Devices
- LCD Displays with Touch Screen
- Input / Output Devices

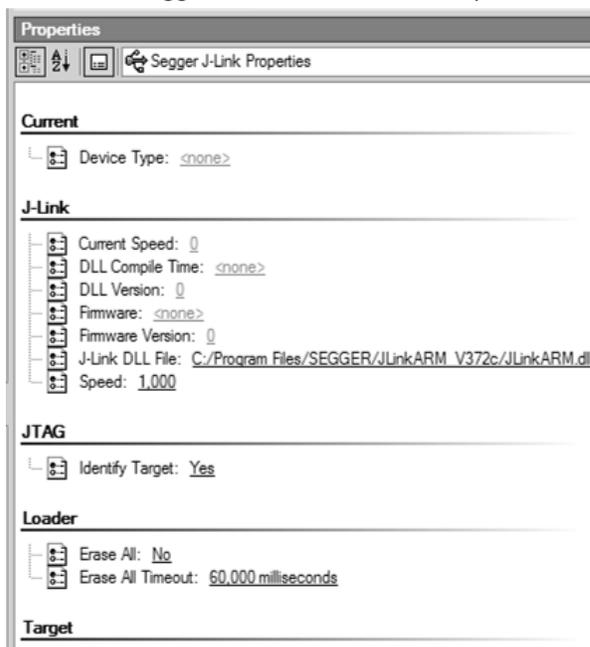
13. Configuring Rowley CrossWorks CrossStudio for ARM[®] for J-Link Flashing

- 1) See the document “μEZ[®] Software Quickstart Guide” for details on how to download the μEZ[®] source code and setup the Rowley CrossWorks compiler. (<http://www.teamfdi.com/development-tools-kits/uez.php>) software quick start guide
- 2) Plug in the J-Link device into the PC and install any drivers as directed. The Segger J-Link drivers can be found at <http://www.segger.com/cms/jlink-software.html> with additional information at <http://www.segger.com/cms/development-tools.html>.
- 3) Plug in the J-Link's JTAG connector to the μEZ GUI board at J3 with the JTAG adapter.

- 4) Select **Target** menu and choose **Targets**. The following list will appear to the right.



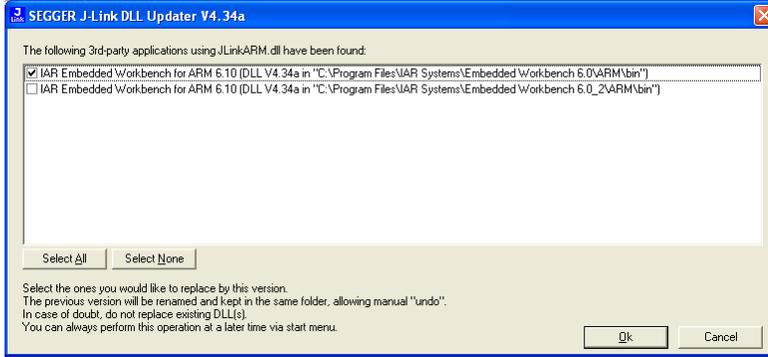
- 5) Right click on "Segger J-Link" and select Properties



- 6) If programming a blank LPC4088 part, select a Speed of 100. If the part has already been programmed, select a Speed of 1000.
- 7) Go back to menu **Target** and select "Connect Segger J-Link"
- 8) Compile the application and press F5 to download and start debugging.

14. Configuring IAR EWARM v6.30 for J-Link Flashing

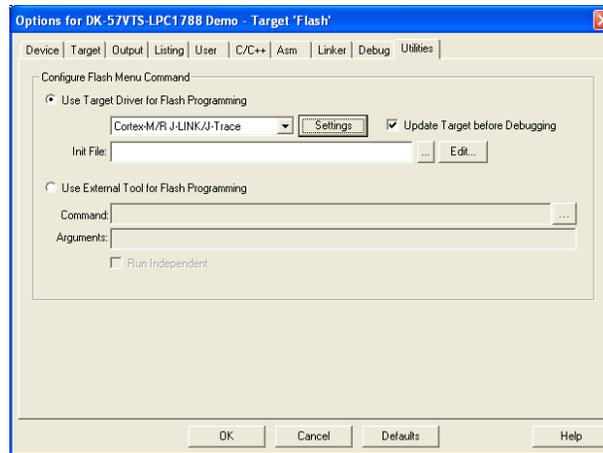
The IAR tools do not require any special configuration for configuring the J-Link tools. To update IAR's J-Link dll you just need to run Segger's J-Link DLL Updated and select the IAR install you wish to update and click Ok.



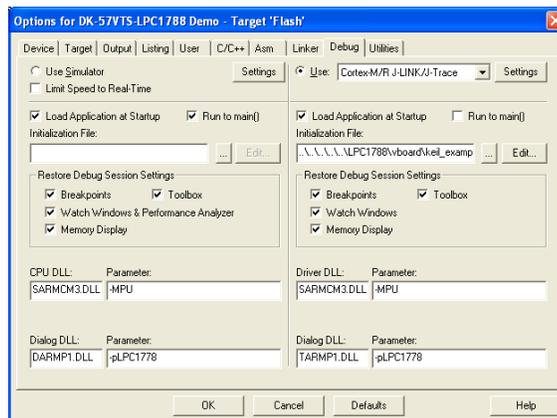
15. Configuring Keil uVison 4 for J-Link Flashing

To configure Keil, use the following steps.

1. Click Flash from the Menu Bar and select Configure Flash Tools...
2. Click the Utilities Tab.
3. In the dropdown box for Use Target Driver for Flash Programming select the CORTEX-M/R J-LINK/J-Trace.



4. Click the Debug tab and select the same device for Use.



16. Functional Test Software

The Functional Test Software tests all the basic features of the uEZGUI-4088-43WQN KIT as follows.

Functional Test is a step by step test of the following parameters:

- Accelerometer – Tests the presence of the accelerometer.
- EEPROM test - The EEPROM is tested for communication and integrity.
- LCD color test - Red, Green, and Blue are displayed in smooth bands to verify the LCD lines
- MicroSD – Looks for a Micro SD Card with the file “SLIDES/SLIDES.TXT”
- NOR Flash Memory test - basic test is performed to confirm read/write access.
- RTC – Sets the time and confirms the clock is running.
- SDRAM size test - Memory is sized and a basic test is performed to confirm read/write access.
- Speaker test - Tones are played and the User is asked to verify that they are heard.
- Temperature – the on board LM75B sensor is tested to be in a range of 20-30 C.

A final report of PASS or FAIL is displayed along with a list of any Skipped and Failed items.

17. Board Layout

The following figure illustrates the layout of the various components of the UEZGUI-4088-43WQN kit. They are for reference only and are subject to change.

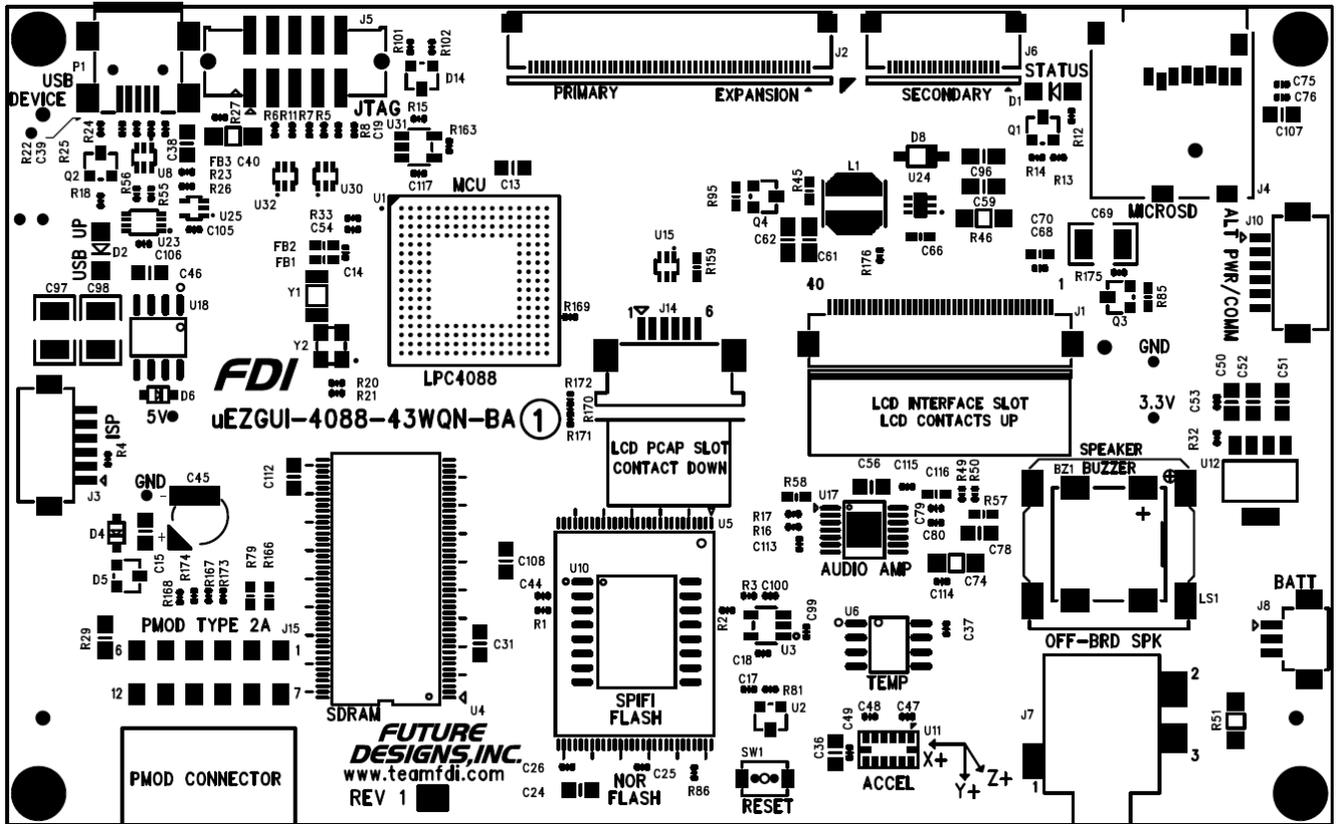


Figure 2 – UEZGUI-4088-43WQN-BA Component View

18. I/O Connector Descriptions

J5 - JTAG Connector

The UEZGUI-4088-43WQN-BA uses a reduced size JTAG connector based on a 2mm Header. This smaller connector provides 100% of the functionality of the standard 20-pin JTAG connector, but utilizes 70% less board space. The connector is a standard part available from most major vendors.

Pin Number	Description	Pin Number	Description
1	GND	6	RTCK
2	5.0V	7	TDI
3	TDO	8	TMS
4	RESETn	9	3.3V
5	TCK	10	TRSTn

The FDI Mini-JTAG Adapter is available here:

<http://www.digikey.com/product-detail/en/JLINK-ARM-AD/622-1039-ND/2631978>

For users that may have existing JTAG debuggers, an adapter may be fabricated using the following wiring diagram: (part numbers for the connectors are included from both the manufacturer and Digi-Key)

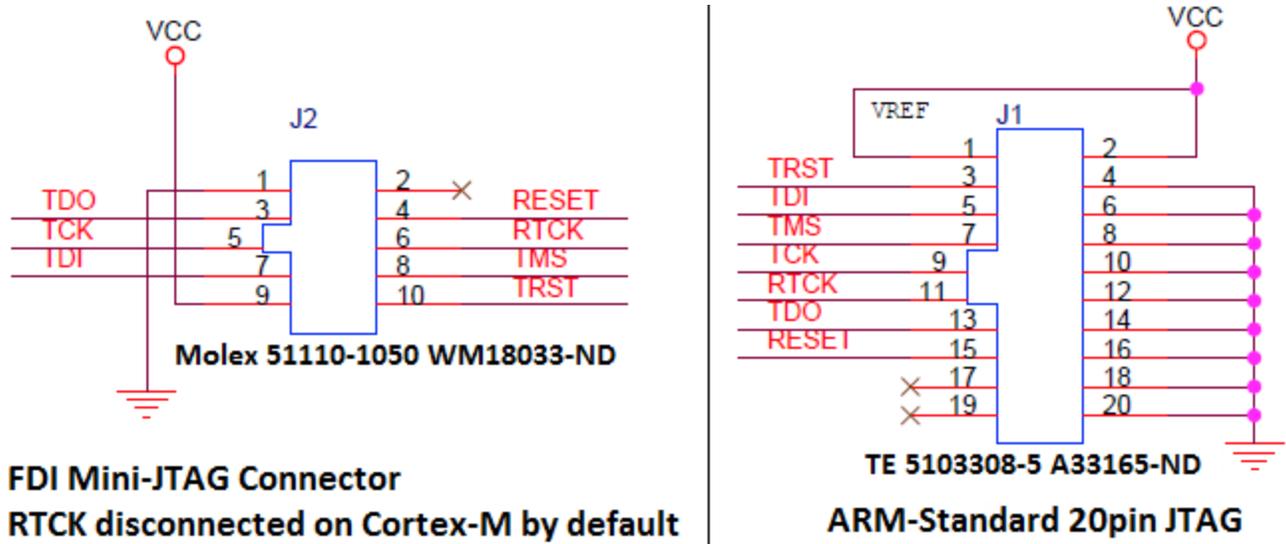


Figure 3 – Mini JTAG Adapter Schematic

J12 - Tag Connect

The UEZGUI-4088-WQN-BA also includes the ability to JTAG and program using the Tag-Connect TC2050-ARM2010 ARM 20-pin to TC2050 Adapter.

- Adapter: <http://www.tag-connect.com/TC2050-ARM2010>
- Cable with legs: <http://www.tag-connect.com/TC2050-IDC>
- Cable with no legs: <http://www.tag-connect.com/TC2050-IDC-NL>
- Holding clip for no-legs cable version: <http://www.tag-connect.com/TC2050-CLIP>



Figure 4 – Tag-Connect JTAG adapter, cables, and pinout for 10-pin TC2050

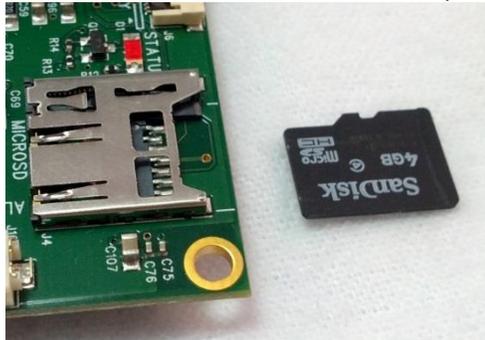
J4 - MicroSD Socket

When connected to the USB Host port on a PC, the UEZGUI-4088-43WQN-BA will appear as a USB Flash Drive to the PC, allowing the user to read and write files directly to the MicroSD card. The unit uses a MicroSD Socket for flexible mass storage capability. μEZ™ supports MicroSDHC and SDHC Cards up to 64GB. This interface uses 4-wire SD mode.

Pin Number	Description
1	Micro SD DAT2
2	Micro SD DAT3
3	Micro SD CMD
4	3.3V
5	Micro SD SCLK
6	Ground
7	Micro SD DAT0
8	Micro SD DAT1

WARNING: The microSD card must only be removed using the spring loaded “push-pull” mechanism on the microSD socket. Improper forceful removal of the microSD card will result in permanent damage to the socket that is not covered under warranty. To insert the card, just push it into the socket until a “click” sound is heard.

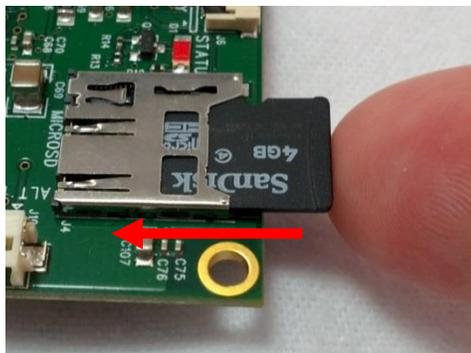
Start with the microSD card in this position relative to the microSD slot with the text and “lip” facing up.



Next gently insert the card partially into the socket.



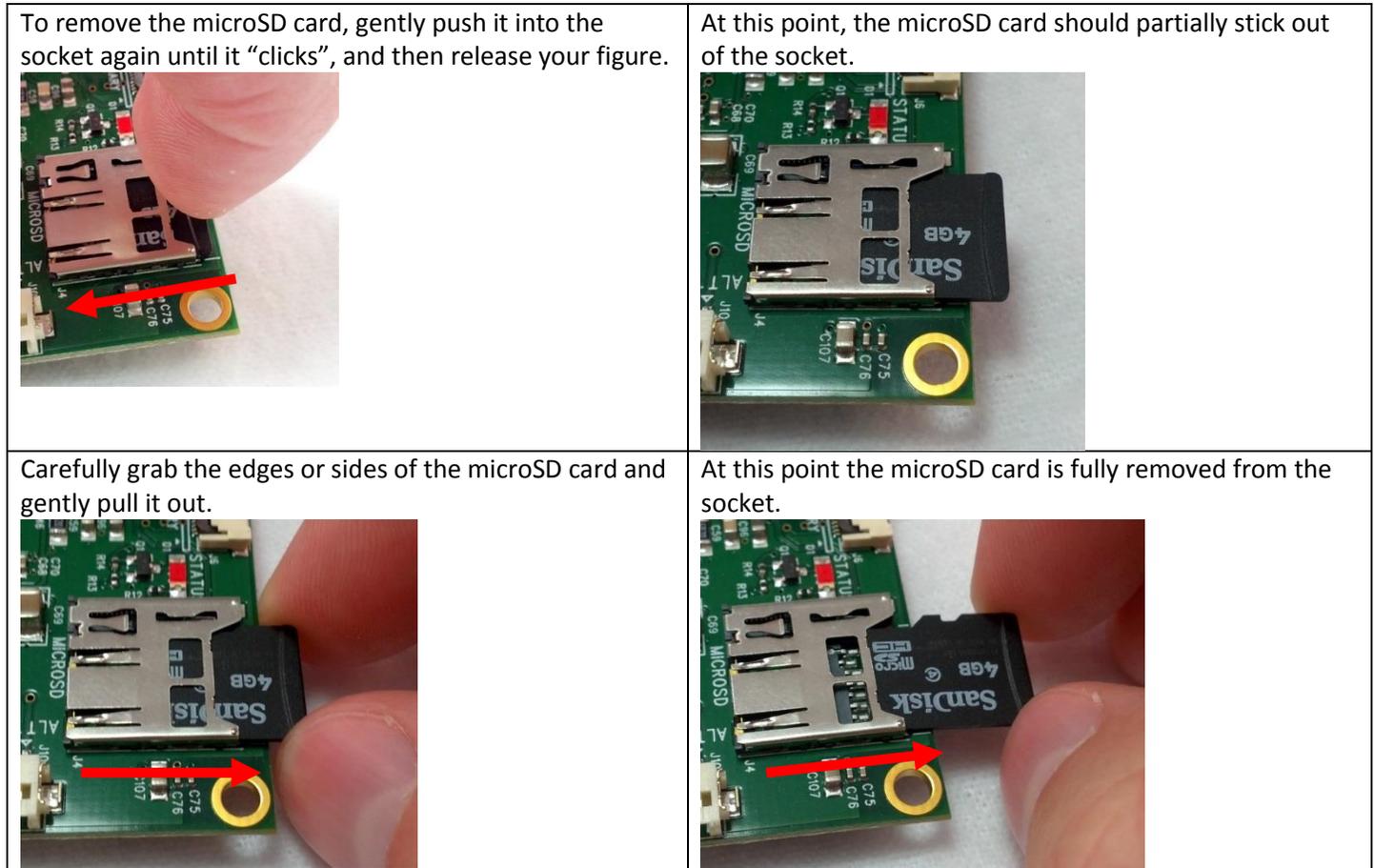
Use your figure to gently push the card into the socket until it clicks.



At this point the microSD card is fully inserted. It should not fall out, even if the unit is shaken vigorously.



To remove the microSD card, press the card back into the socket until another “click” sound is heard, then release pressure on the card. At this point, the card should be partially ejected from the socket. Finally grab the card and gently pull to remove it. See the following pictures for proper micro-SD removal:



J3 - ISP Connector

The UEZGUI-4088-43WQN-BA includes an ISP programming header. This header is designed to be utilized with the USB-ICP-LPC2K programmer from FDI. However, the connector on the UEZGUI-4088-43WQN-BA is not directly compatible with the one used on the USB-ICP. This connector is a 1.25mm Hirose male, shrouded connector. The Hirose Part Number is: DF13A-6P-1.25H(20) (Digikey PN: H3371-ND). The pin out shown below is a direct, 1:1 connection to the USB-ICP-LPC2K programmer available from Future Designs, Digi-Key or Mouser.

Pin Number	Description
1	3.3V
2	Reset Input
3	ISP Entry
4	Ground
5	RXD
6	TXD

J10 - Alternate Power and communication

The UEZGUI-4088-43WQN-BA includes an alternate power and communication header. This header provides access to UART 3, as well as a 5V power input. This connector is a 1.25mm Hirose male, shrouded connector. The Hirose Part Number is: DF13A-6P-1.25H(20) (Digikey PN: H3371-ND).

Pin Number	Description
1	P0.0_TXD3
2	5V Input
3	5V Input
4	Ground
5	Ground
6	P0.1_RXD3

J15 - PMOD Type 2A Connector

The UEZGUI-4088-43WQN-BA includes a PMOD Type 2A (SPI) connection to an expansion board.

Pin Number	Description	Pin Number	Description
1	SPI Chip Select	7	Interrupt Request
2	SPI MOSI	8	Reset
3	SPI MISO	9	Optional I2C SCL
4	SPI Clock	10	Optional I2C SDA
5	Ground	11	Ground
6	3.3V	12	3.3V

P1 – USB mini-AB port and USB Power Input 5VDC

The UEZGUI-4088-43WQN-BA has a mini-AB USB connector for host or device mode. By using a USB OTG adapter (with a mini-A plug) it will short the ID pin 4 to ground. This can be used for host mode detection in the application. The UEZGUI-4088-43WQN -BA is normally powered via P1 with the included 5V USB Wall Supply or via a standard 500mA powered USB port. This power supply is only included in the development kit and is not included with the UEZGUI-4088-43WQN -BA.

Pin Number	Description
1	5V
2	D-
3	D+
4	ID
5	Ground

J2 & J6 - Expansion Connectors

The UEZGUI-4088-43WQN-BA includes two expansion connectors that provide a wide variety of capabilities for user expansion, ranging from 10/100 Ethernet to USB Host, etc. **Please Note:** When using I/O signals on the μEZ GUI Expansion Connectors (J2 & J6) to connect via the customers Expansion Board to external connectors or signals, it is the customer's responsibility to provide adequate ESD protection and filtering to prevent damage to any pins that are not directly protected on the μEZ GUI.

The tables below provide the pinout and signal names available on these connectors:

J2 Signal Details

Pin	Pin Name	Pin Description	
1	Ground (GND)		Power
2	P0.11_RXD2_SCL2_MAT3.1⁽¹⁾ Unavailable	P0[11] - General purpose digital Input/Output pin.	I/O
		RXD2 - Receiver input for UART2.	I
		SCL2 - I2C2 clock Input/Output (this is not an open-drain pin)	I/O
		MAT3[1] -Match output for Timer3, channel 1.	O
3	P0.10_TXD2_SDA2_MAT3 (Not usable in default configuration!)	P0[10] - General purpose digital input/output pin.	I/O
		SDA2 - I2C2 data input/output (this is not an open-drain pin).	I/O
		MAT3[0] - Match output for Timer3, channel 0.	O
		TXD2 -Transmitter output for UART2.	O
4	P2.21	P2[21] - General purpose digital input/output pin.	I/O
5	P0.21	P0[21] - General purpose digital input/output pin.	I/O
		U1_RI - Ring Indicator input for UART 1	I
		RD1 - CAN1 receive input	I
		U4_SCLK - UART 4 Clock/output in synchronous mode	I/O
		U4_OE - UART 4 Output Enable for RS485/EIA-485	O
6	P4.21_SCL2_SSEL1	P4[21] - General purpose digital input/output pin.	I/O
		SSEL1 - Slave Select for SSP1.	I/O
		SCL2 - I2C2 clock input/output (this is not an open-drain pin)	I/O
7	P4.20_SDA2_SCK1	P4[20] - General purpose digital input/output pin.	I/O
		SCK1 - Serial Clock for SSP1.	I/O
		SDA2 - I2C2 data input/output (this is not an open-drain pin).	I/O
8	P4.23_RXD2_MOSI1	P4[23] - General purpose digital input/output pin.	I/O
		RXD2 - Receiver input for UART2.	I
		MOSI1 - Master Out Slave In for SSP1	O
9	P4.22_TXD2_MISO1	P4[22] - General purpose digital input/output pin.	I/O
		TXD2 -Transmitter output for UART2.	O
		MISO1 - Master In Slave Out for SSP1.	I/O
10	Ground (GND)		Power
11	USB1_DM	P0[30] - General purpose digital Input/Output pin.	I/O
		USB_D- 1 - USB port 1 bidirectional D- line.	I/O
12	USB1_DP	P4[29] - General purpose digital Input/Output pin.	I/O
		USB_D+1 - USB port 1 bidirectional D+ line.	I/O
13	USB1H_PWRD	P4[26] -General purpose digital Input/Output pin.	I/O
		BLS0 - LOW active Byte Lane select signal 0.	O
14	USB1H_OVC	P4[24] - General purpose digital Input/Output pin.	I/O
15	USB1H_PPWR	P0[19] - General purpose digital Input/Output pin.	I/O
		CAP1[1] - Capture input for Timer 1, channel 1	I
		USB_PPWR1 - Port Power enable signal for USB port 1.	O
16	P0.9_I2STX_SDA_MOSI1_MAT2.3	I/O	I/O
		I2STX_SDA - I2S transmit data. It is driven by the transmitter and read by the receiver. Corresponds to the signal SD in the I2S-bus specification.	I/O
		MAT2[3] - Match output for Timer 2, channel 3	O
		MOSI1 - Master Out Slave In for SSP1.	I/O
17	P0.8_I2STX_WS_MISO1_MAT 2.2	P0[8] -General purpose digital Input/Output pin.	I/O
		I2STX_WS - I2S Transmit word select. It is driven by the master and received by the slave. Corresponds to the signal WS in the I2S-bus specification.	I/O
		MAT2[2] - Match output for Timer 2, channel 2	O
		MISO1 - Master In Slave Out for SSP1.	I/O
18	P0.7_I2STX_CLK_SCK1_MAT 2.1	P0[7] – General purpose digital Input/Output pin.	I/O
		I2STX_CLK - I2S transmit clock. It is driven by the master and received by the slave. Corresponds to the signal SCK in the I2S-bus specification.	I/O

Pin	Pin Name	Pin Description	
		MAT2[1] - Match output for Timer 2, channel 1	O
		SCK1 - Serial Clock for SSP1.	I/O
19	P0.6_I2SRX_SDA_SSEL1_MAT2.0	P0[6] - General purpose digital Input/Output pin	I/O
		I2SRX_SDA - I2S Receive data. It is driven by the transmitter and read by the receiver. Corresponds to the signal SD in the I2S-bus specification.	I/O
		SSEL1 - Slave Select for SSP1.	I/O
		MAT2[0] - Match output for Timer 2, channel 0	O
20	P0.5_I2SRX_WS_TD2_CAP2.1	P0[5] - General purpose digital Input/Output pin.	I/O
		I2SRX_WS - I2S Receive word select. It is driven by the master and received by the slave. Corresponds to the signal WS in the I2S-bus specification.	I/O
		TD2 - CAN2 transmitter output.	O
		CAP2[1] - Capture input for Timer 2, channel 1	I
21	P0.4_I2SRX_CLK_RD2_CAP2.0	P0[4] - General purpose digital Input/Output pin.	I/O
		I2SRX_CLK - I2S Receive clock. It is driven by the master and received by the slave. Corresponds to the signal SCK in the I2S-bus specification.	I/O
		RD2 - CAN2 receiver input	I
		CAP2[0] - Capture input for Timer 2, channel 0	I
22	Ground (GND)		Power
23	RESET_INn	External reset input: A LOW on this pin resets the device, causing I/O ports and peripherals to take on their default states, and processor execution to begin at address 0. TTL with hysteresis, 5 V tolerant	I
24	RESET_OUTn	RSTOUT - This is a 3.3 V pin. LOW on this pin indicates LPC4088 being in Reset state	O
25	P0.26_AD03_AOUT_RXD3 ⁽¹⁾	P0[26] General purpose digital Input/Output pin.	I
		AD0[3] - A/D converter 0, input 3.	O
		AOUT - D/A converter output.	I
		RXD3 - Receiver input for UART3	I/O
26	P1.31_SCK1_AD0.5	P1[31] - General purpose digital Input/Output pin.	I/O
		SCK1 - Serial Clock for SSP1.	I/O
		AD0[5] - A/D converter 0, input 5	I
27	P1.17_ENET_MDIO	P1[17] - General purpose digital Input/Output pin.	I/O
		ENET_MDIO - Ethernet MIIM data input and Output	I/O
28	P1.16_ENET_MDC	P1[16] - General purpose digital Input/Output pin.	I/O
		ENET_MDC - Ethernet MIIM clock	O
29	Ground (GND)		Power
30	P1.15_ENET_REFCLK	P1[15] - General purpose digital Input/Output pin.	I/O
		ENET_REF_CLK/ENET_RX_CLK - Ethernet Reference Clock (RMII interface)/ Ethernet Receive Clock (MII interface)	I
31	P1.14_ENET_RX_ER	P1[14] - General purpose digital Input/Output pin.	I/O
		ENET_RX_ER - Ethernet receive error (RMII/MII interface)	I
32	3p3 volts	3.3 volts	Power
33	P1.10_ENET_RXD1	P1[10] - General purpose digital Input/Output pin.	I/O
		ENET_RXD1 - Ethernet receive data 1 (RMII/MII interface)	I
34	P1.9_ENET_RXD0	P1[9] - General purpose digital Input/Output pin.	I/O
		ENET_RXD0 - Ethernet receive data 0 (RMII/MII interface)	I
35	P1.8_ENET_CRSDV	P1[8] - General purpose digital Input/Output pin.	I/O
		ENET_CRSDV/ENET_CRSDV - Ethernet Carrier Sense/Data Valid (RMII interface)/ Ethernet Carrier Sense (MII interface)	I
36	P1.4_ENET_TXEN	P1[4] - General purpose digital Input/Output pin.	I/O
		ENET_TX_EN - Ethernet transmit data enable (RMII/MII interface)	O
37	P1.1_ENET_TXD1	P1[1] - General purpose digital Input/Output pin.	I/O
		ENET_TXD1 - Ethernet transmit data 1 (RMII/MII interface)	O
38	P1.0_ENET_TXD0	P1[0] - General purpose Digital Input/Output pin.	I/O
		ENET_TXD0 - Ethernet transmit data 0 (RMII/MII interface)	O

Pin	Pin Name	Pin Description	
39	Ground (GND)		Power
40	P2.10_EINT0_NMI	I/O - P2[10] - General purpose digital Input/Output pin. Note: LOW on this pin while RESET is LOW forces on-chip boot loader to take over control of the part after a reset.	I/O
41	P0.3_RXD0	P0[3] - General purpose digital Input/Output pin.	I/O
		RXD0 - Receiver input for UART0	I
42	P0.2_TXD0	P0[2] - General purpose digital Input/Output pin.	I/O
		TXD0 - Transmitter output for UART0	O
43	USBD_DP	USB_D+2 - USB port 2 bidirectional D+ line	I/O
44	USBD_DM	USB_D-2 - USB port 2 bidirectional D- line	I/O
45	USBD_VBUS	USB_PWRD2 - Power Status for USB port 2.	I
		VBUS - Monitors the presence of USB bus power. Note: This signal must be HIGH for USB reset to occur. I - AD0[4] - A/D converter 0, input 4	I
46	5volts (5V0)	5.0 Volts	Power
47	5volts (5V0)	5.0 Volts	Power
48	5volts (5V0)	5.0 Volts	Power
49	3p3 volts (3V3)	3.3 Volts	Power
50	3p3 volts (3V3)	3.3 Volts	Power

On the UEZGUI-4088-43WQN, J2-6 through J2-9 can be selectable for either the group of Port 0 pins, or Port 4 pins shown below. By default, the SPIFI Flash is loaded along with RN2. This allows the use of P4.20 – P4.23 on the expansion connector. The following 0 ohm resistor networks are used to control which port pins are used:

Pin on Expansion board	Pin on NOR Flash/SPIFI Flash	RN1	RN2
P4.20 – P4.23	P0.15 - P0.18	No Load	Loaded
P0.15 - P0.18	P4.20 – P4.23	Loaded	No Load

J6 Signal Details

Pin	Pin Name	Pin Description	
1	Ground (GND)		Power
2	P5.4_TXD0_OE_MAT3.3_TXD4	P5[4] – General Purpose digital Input/Output	I/O
		TXD0_OE - UART0 Transmitter Output Enable	O
		MAT3[3] – Match output for Timer 3, channel 3	O
		TXD4 – UART4 Transmit data	O
3	P5.3_RXD4_SCL0+	P5[3] – General Purpose digital Input/Output	I/O
		RXD4- UART4 receive data	I
		SCL0+ - I2C Clock for FM+ Operation	I/O
4	P5.2_MAT3.2_SDA0+	P5[3] – General Purpose digital Input/Output	I/O
		MAT3[2] – Match output for Timer 3, channel 2	O
		SDA0+ - I2C Data for FM+ Operation	I/O
5		Signal needed for high speed SD card so unavailable	
6		Signal needed for high speed SD card so unavailable	
7		Signal needed for high speed SD card so unavailable	
8		Signal needed for high speed SD card so unavailable	
9	P1.5_MCIPWR_PWM0.3	P1[5] – General Purpose digital Input/Output	I/O
		MCIPWR – Power Supply Enable for external SD/MMC Power Supply	O
		PWM0[3]-Pulse Width Modulator 0, output 3	O
10	P1.3_MCICMD-PWM0.2	P1[3] – General Purpose digital Input/Output	I/O
		MCICMD – Command line for SD/MMC interface	I/O
		PWM0[2]-Pulse Width Modulator 0, output 2	O

Pin	Pin Name	Pin Description	
11	P0.1_TD1_RXD3_RXD0	P0[1] – General Purpose digital Input/Output	I/O
		TD1 – Can1 transmitter output	O
		RXD3 – Receiver input for UART3	I
		RXD0 – Alternate UART0 receive data	I
12	P0.0_RD1_TXD3_TXD0	P0[0] – General Purpose digital Input/Output	I/O
		RD1 – Can1 receive input	I
		TXD3- Transmitter output for UART3	O
		TXD0 – alternate UART0 transmit data	O
13	5volts(5VO)	5.0 Volts	Power
14	Ground (GND)		Power
15	<i>P0.13_USB2_UPLD_AD0.7⁽¹⁾</i>	P0[13] – General Purpose digital Input/Output	I/O
		UPLD- USB port 2 Good Link indicator	O
		AD0[7]- A/D converter0, input 7	I
16	<i>P0.12_USBPPWR2_AD0.6⁽¹⁾</i>	P0[12] – General Purpose digital Input/Output	I/O
		AD0[6]- A/D converter0, input 6	I
17	<i>P0.25_AD0.2_TXD3⁽¹⁾</i>	P0[25] – General Purpose digital Input/Output	I/O
		AD0[2]- A/D converter0, input 2	I
		TXD3 – Transmitter output for UART3	O
18	<i>TP_RL_Y2_P0.24_AD0.1⁽¹⁾</i>	TP_RL_Y2 – Touch panel interface right side horizontal [Not Typically Supported]	
		P0[24] - General Purpose digital Input/Output	I/O
		AD0[1] – A/D converter0, input 1	I
19	<i>TP_RL_X1_P0.23_AD0.0⁽¹⁾</i>	TP_RL_X1 – Touch Panel left side horizontal [Not Typically Supported]	
		P0[23] - General Purpose digital Input/Output	I/O
		AD0[0] – A/D converter0, input 0	I
20	Ground (GND)		Power

Notes: (1) Signal may be utilized by on-board function. Consult schematic for connectivity and actual usage/availability

J2 & J6 - Expansion Connector Cable Details

The maximum length for the expansion connector cables is as follows:

General Purpose IO, TTL, Serial, etc = 6” recommended maximum, 8” absolute maximum

Ethernet, high-speed IO, etc = 3” recommended maximum, 4” absolute maximum

The following table provides example part numbers for the expansion cables:

Description	Mfg	Mfg PN	Digi-Key Pn
3” 20-pin 0.5mm	Molex	21020-0209	WM10226-ND
6” 20-pin 0.5mm	Molex	21020-0215	WM10218-ND
3” 50-pin 0.5mm	Molex	21020-7650	WM10231-ND
6” 50-pin 0.5mm	Molex	21020-0548	WM10223-ND

Note: These lengths are only recommendations. The actual lengths utilized depend on the expansion board circuitry, layouts and general environment of the application. Tests and validation should be conducted to ensure the functional operation and use of all third party expansion connectors.

19. Schematics

Please see the FDI website at <http://www.teamfdi.com/support/touch-screen.php#uezgui-4088-43wqn> for support documentation.

20. Temperature Range

UEZGUI-4088-43WQN-BA board w/o LCD: -40°C to +85°C

UEZGUI-4088-43WQN-BA with LCD: -10°C to +70°C

21. ESD Warning



The UEZGUI-4088-43WQN-BA kit is shipped in a protective anti-static package. The kit must not be subjected to high electrostatic potentials. Damage may occur to the boards that will not be covered under warranty. General practice for working with static sensitive devices should be followed when working with the kit.

22. Real Time Clock Backup Time

The μEZ GUI's Real Time Clock is backed up with a Seiko Super Capacitor to allow the time to be preserved when external power is removed. The calculated backup time is shown below.

Super Capacitor	Typical Voltage	Stop Voltage	Maximum Current	Typical Backup Time
XH414HG	3.0 V	2.0 V	1μA	18 hrs

23. Power Requirements

Power is supplied into the mini-USB connector (P1) via the USB cable and power supply provided in the kit. The power supply provides 5VDC output at 1.2A (min) and has input voltage range of 100-240VAC with standard U.S. 2-prong plug. The following typical power requirements were measured at room temperature with LPC1788 at 120MHz clock rate:

Voltage	μEZ Demo Screen	Observed Max	Maximum Allowed
5V	309 mA	330 mA	Up to 2A through power connectors

μEZ GUI USB Input Port Power Requirements:

- +5VDC ±5% is the input power range specification. However, since the 5VDC input has reverse diode protection, it may be necessary to provide a higher input voltage level of 5VDC input to ensure that the μEZ GUI 5VDC output level retains the specified tolerance. If the μEZ GUI input level drops to 4.75VDC, then the μEZ GUI +5VDC output level to either the Expansion Board or the USB Host connector may be lower than 4.75VDC since the worst case drop from input to output is typically 0.25V.
- For reference on the USB output port from the μEZGUI, the following are the specifications:
 - USB High Power Specifications are 500mA maximum, and 4.75V to 5.25V standard.
 - USB Low Power Specifications are 100mA maximum, and 4.4V to 5.25V standard.

- The μEZ GUI is capable of providing a maximum of 300mA of 3.3V power for “external use” over the expansion connectors. If more than 300mA of 3.3V is needed for an expansion board;
 - Then the primary power input (i.e. 5V) should be located on the expansion board rather than on the UEZGUI.
 - The expansion board should be designed with a separate 3.3V voltage regulator.
 - Ensure the 3.3V voltage rails of the μEZ GUI & Expansion Board are not connected together.
 - The μEZ GUI should be powered using 5V from the expansion board over the 70 pin breakout, instead of powering the expansion board from the μEZ GUI unit.
- The UEZGUI-4088-43WQN may also optionally be powered via the following connectors;
 - Alternate Power/Communication Connector, J10, with a maximum of 2A 5V input
 - Expansion Connector(s) J2 and/or J6 – refer to the expansion connector section for details

24. Mechanical Details

The following illustrations show the mechanical details of the UEZGUI-4088-43WQN-BA PCB.

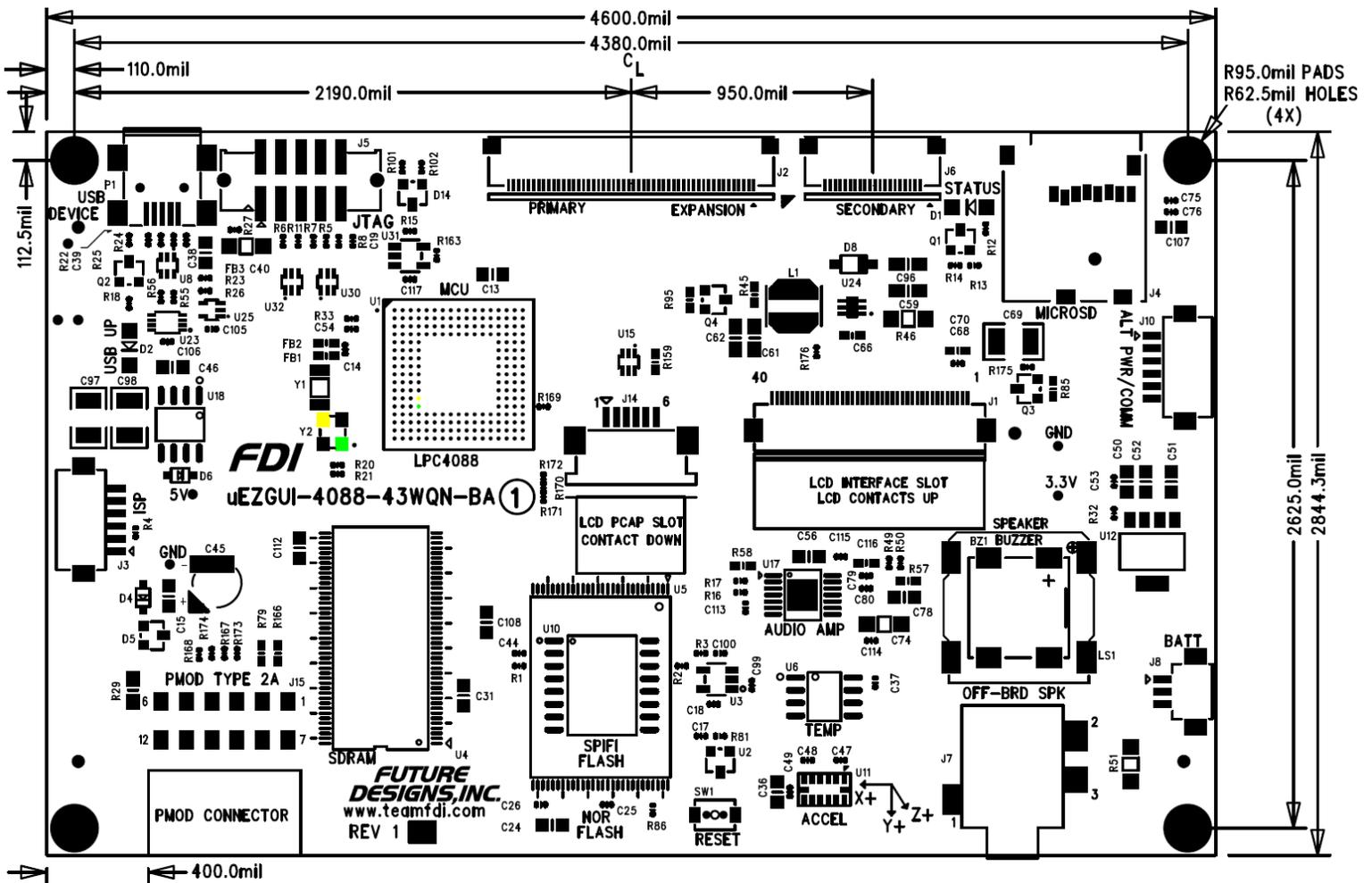


Figure 5 –Mechanical Dimensions (Component View)

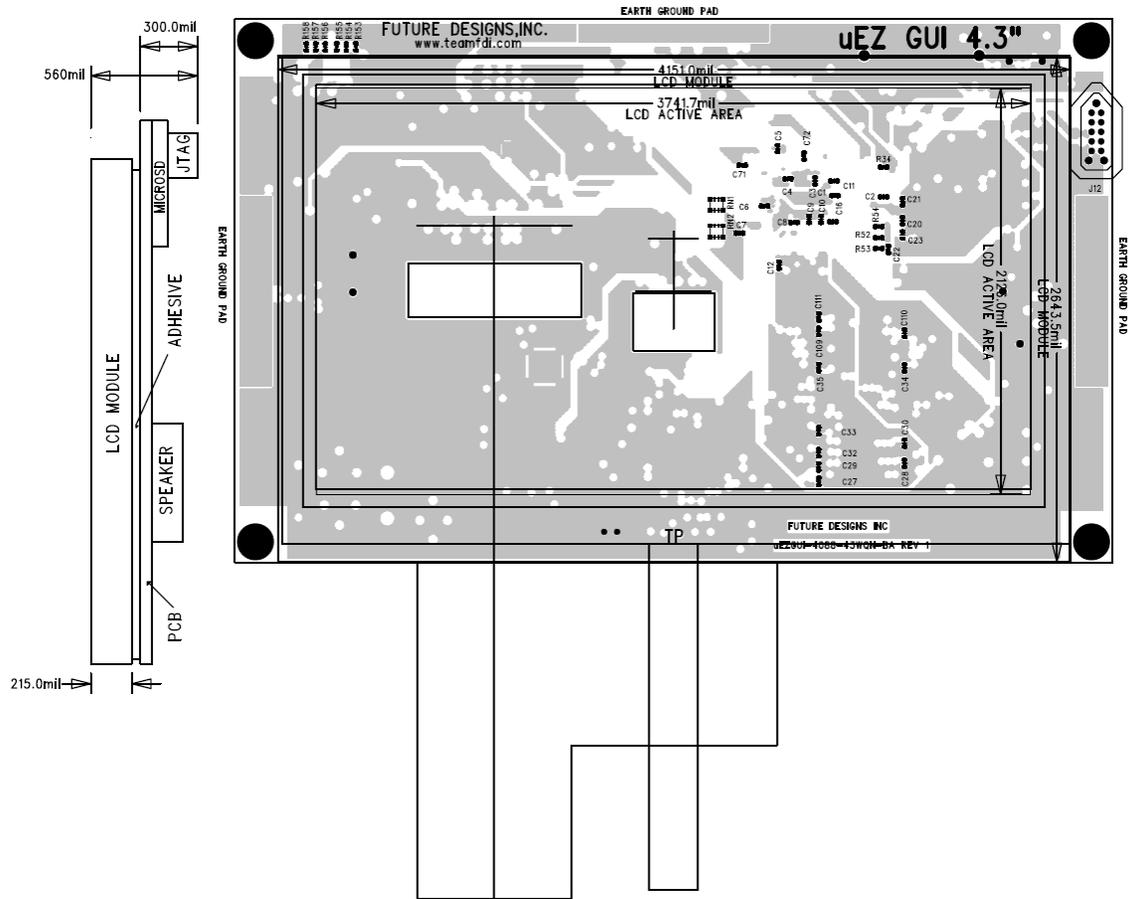


Figure 6 –Mechanical Dimensions (LCD Module View and Side View)

25. Updating to UEZGUI-4088-43WQN-BA from UEZGUI-1788-43WQR-BA

The UEZGUI-4088-43WQN-BA has a Newhaven display that is approximately the same height and depth as the one found on the UEZGUI-1788-43WQR model. Here is a stack up comparison between the two units. The Newhaven display is about 0.3mm thinner on its total horizontal width. In general most assemblies will not need any modification to move between the UEZGUI-1788-43WQR and the UEZGUI-4088-43WQN.

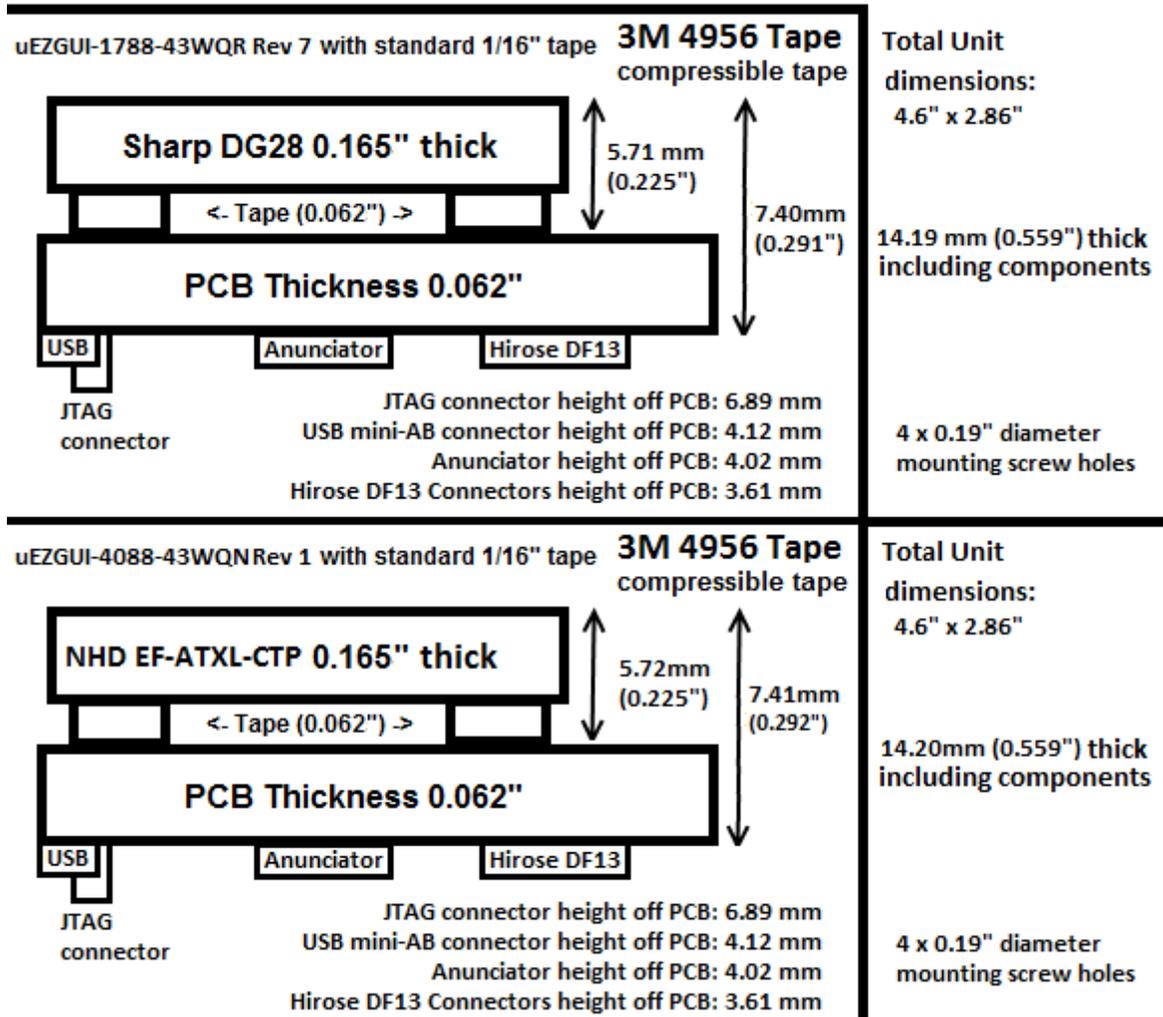


Figure 7 – UEZGUI 43WQN-BA vs. 43WQR-BA Stack-up Comparison